

CODING CIRCUIT

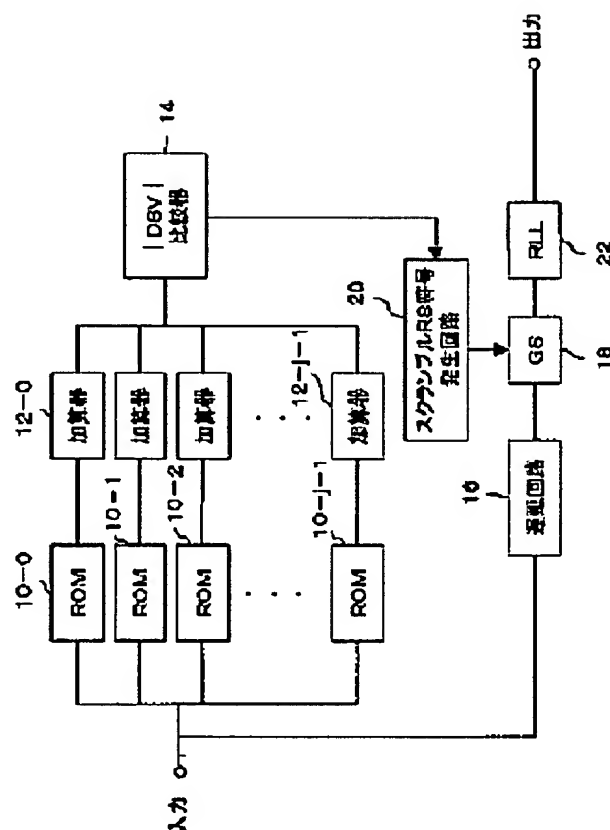
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Abstract of JP2000134101

PROBLEM TO BE SOLVED: To allow a circuit to efficiently conduct an arithmetic operation for a plurality of code word candidates.

SOLUTION: This circuit receives one information word and ROMs 10-0 to 10-j-1 output DC components CDS of a plurality of code words, in response to the information word. Adds 12-0 to 12-j-1 sum the CDS respectively to obtain DSV, and a &verbar DSV&verbar comparator 14 detects the minimum DSV. Then a GS circuit 18 and an RLL circuit 22 generate and output a code word with the selected minimum DSV.



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